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TO: Commissioner for Patents
Attn: Examiner Le Hien Luu
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Alexandria, VA 22313-1450

FROM: George H. Gates
OUR REF.: 5104.05
TELEPHONE: (310) 642-4146

Total pages, including cover letter: 5

PTO FAX NUMBER: 703-872-9306

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Title of Document Transmitted:	TRANSMITTAL SHEETS AND AMENDMENT UNDER 37 C.F.R. §1.312
Applicant:	Robert J. McMillen et al.
Serial No.:	09/783,923
Filed:	February 14, 2001
Group Art Unit:	2152
Title:	MULTICAST TRANSMISSIONS IN A MULTISTAGE INTERCONNECT NETWORK
Our Ref. No.:	5104.05

Please charge all fees to Deposit Account No. 50-1673 of NCR Corporation, the assignee of the present application.

By: George H. Gates
Name: George H. Gates
Reg. No.: 33,500

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Barbara Senty
Signature

April 26, 2005
Date

Notice of Allow. Date: March 17, 2005
Confirmation No.: 1479

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: Robert J. McMillen et al. Examiner: Le Hien Luu
Serial No.: 09/783,923 Group Art Unit: 2152
Filed: February 14, 2001 Docket: 5104.05
Title: MULTICAST TRANSMISSIONS IN A MULTISTAGE INTERCONNECT NETWORK

CERTIFICATE OF MAILING OR TRANSMISSION UNDER 37 CFR 1.8

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By: [Signature]
Name: George H. Gates

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Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Dear Sir:

We are transmitting herewith the attached:

- ☒ Transmittal sheet, in duplicate, containing a Certificate of Mailing or Transmission under 37 CFR 1.8.
- ☒ Amendment Under 37 C.F.R. §1.312.

CLAIMS PRESENT

Claims Remaining:	Highest Number Previously Paid For:	Number Extra	Rate	Fee
Total Claims				
			x \$50.00	= \$
Independent Claims				
			x \$200.00	= \$
MULTIPLE DEPENDENT CLAIM FEE				\$0.00
TOTAL FILING FEE				\$

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Customer Number 22462
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GHG/bjs

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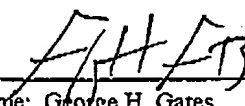
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NOTICE OF ALLOWANCE DATE: March 17, 2005

ISSUE FEE DUE DATE: June 17, 2005

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AMENDMENT UNDER 37 C.F.R. §1.312

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Dear Sir:

In accordance with 37 C.F.R. §1.312, and without withdrawing the application from
issue, please amend the above-identified application as follows:

IN THE SPECIFICATION

Please amend the paragraph beginning at page 105, line 9 as follows:

This concludes the description of the preferred embodiment of the invention. In
summary, the present invention comprises a multistage interconnect network 14 (MIN) capable
of supporting massively parallel processing, including point-to-point and multicast
communications between processor modules (PMs) which are connected to the input and output
ports of the network. The network 14 is built using interconnected 8x8 switch nodes 16 arranged
in $\lceil \log_8 N \rceil \lceil \log_8 N \rceil$ stages, wherein N is the number of input/output ports in the network and
 $\lceil \log_8 N \rceil \lceil \log_8 N \rceil$ indicates a ceiling function providing the smallest integer not less than \log_8